



INTEL® INNOVATION DAY



ИСПОЛЬЗОВАНИЕ ИНСТРУМЕНТОВ INTEL В ЦИКЛЕ РАЗРАБОТКИ ПРОГРАММНОГО ОБЕСПЕЧЕНИЯ

Сивков, Дмитрий, Анатольевич

Dmitry.Sivkov@intel.com

Разработка ПО

Разработка программного обеспечения ([англ. software development](#)) — деятельность по созданию нового [программного обеспечения](#)^[1].

Разработка программного обеспечения как [инженерная дисциплина](#) является составной частью (областью) [программной инженерии](#), наряду с дисциплинами, отвечающими за функционирование и [сопровождение](#) программных продуктов^[2].

https://ru.wikipedia.org/wiki/Разработка_программного_обеспечения

CI: Автоматизированная сборка

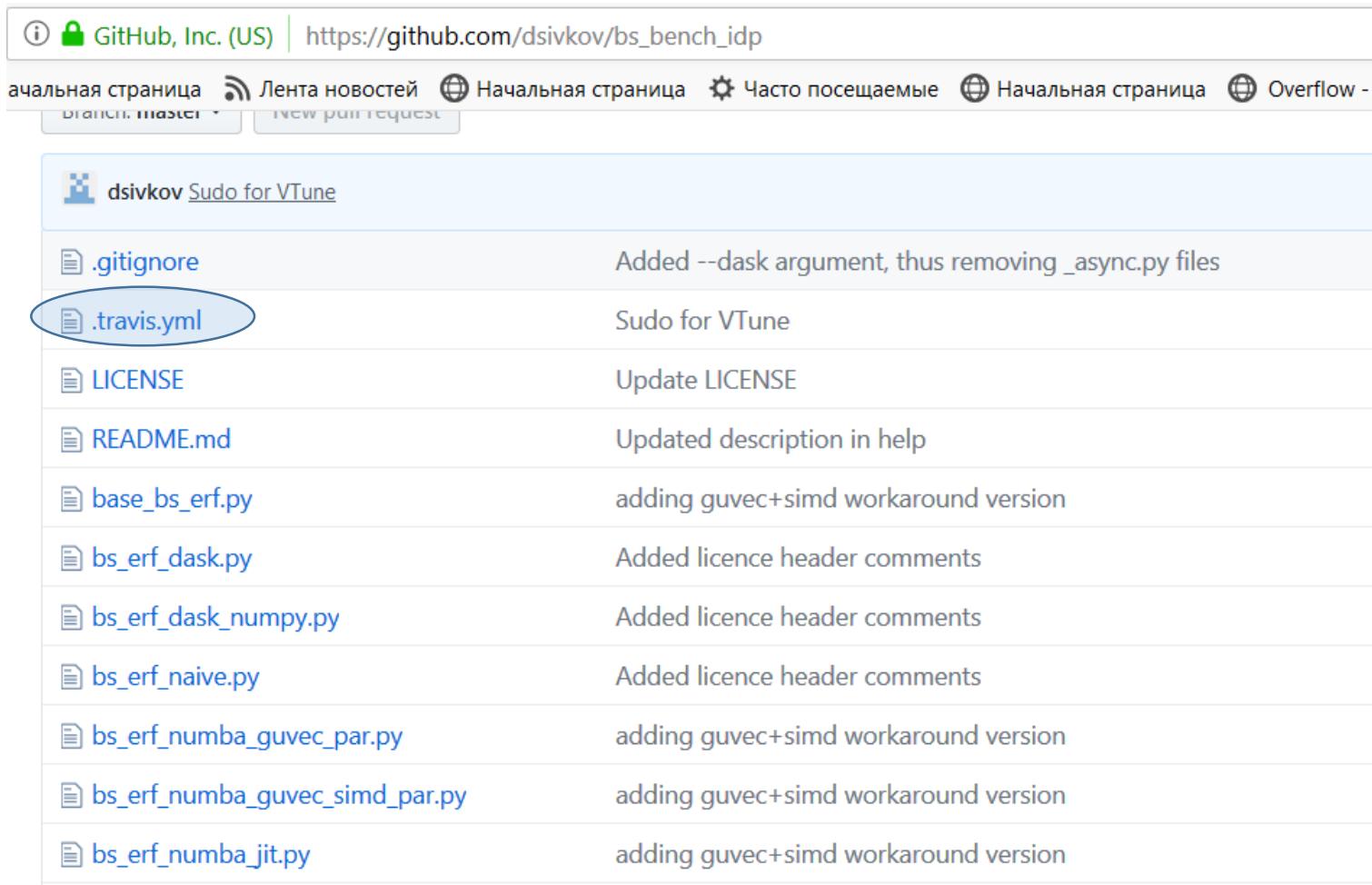
Меньше шагов – меньше вероятность ошибки

Подразумевает явное описание необходимого окружения и зависимостей

Примеры: make, ant, ...

10 минут (включая запуск тестов)

Инструменты Intel в Travis-CI



GitHub, Inc. (US) | https://github.com/dsivkov/bs_bench_idp

Начальная страница | Лента новостей | Начальная страница | Часто посещаемые | Начальная страница | Overflow -

Branch: master | New pull requests

dsivkov Sudo for VTune

.gitignore	Added --dask argument, thus removing _async.py files
.travis.yml	Sudo for VTune
LICENSE	Update LICENSE
README.md	Updated description in help
base_bs_erf.py	adding guvec+simd workaround version
bs_erf_dask.py	Added licence header comments
bs_erf_dask_numpy.py	Added licence header comments
bs_erf_naive.py	Added licence header comments
bs_erf_numba_guvec_par.py	adding guvec+simd workaround version
bs_erf_numba_guvec_simd_par.py	adding guvec+simd workaround version
bs_erf_numba_jit.py	adding guvec+simd workaround version

Инструменты Intel в Travis-CI

Библиотеки и компиляторы

language: python

python:

- "3.6"
- wget https://repo.continuum.io/miniconda/Miniconda3-latest-Linux-x86_64.sh -o miniconda.sh;
- bash miniconda.sh -b -p \$HOME/miniconda
- export PATH="\$HOME/miniconda/bin:\$PATH"
- conda create -y -n intel3 -c intel python=3 numpy numexpr scipy tbb dask numba cython

Инструменты Intel в Travis-CI

Инструменты анализа – Intel® VTune™

- wget http://registrationcenter-download.intel.com/akdlm/irc_nas/tec/13079/vtune_amplifier_2018_update3.tar.gz
 - tar -xzf ./vtune_amplifier_2018_update3.tar.gz
 - sudo ./vtune_amplifier_2018_update3/install.sh -s ./vtune2018u3.cfg
 - sudo sh -c 'echo 0 >/proc/sys/kernel/yama/ptrace_scope'
 - source /opt/intel/vtune_amplifier/amplxe-vars.sh

Инструменты Intel в Travis-CI

Инструменты анализа – Intel® VTune™

script:

- source activate intel3
- python bs_erf_numpy.py --steps 4
- amplxe-cl -collect hotspots -strategy ldconfig.real:notrace:trace,ldconfig:notrace:trace -result-dir result -- python bs_erf_naive.py --steps 4
- amplxe-cl -R hotspots -result-dir result

Инструменты Intel в Travis-CI

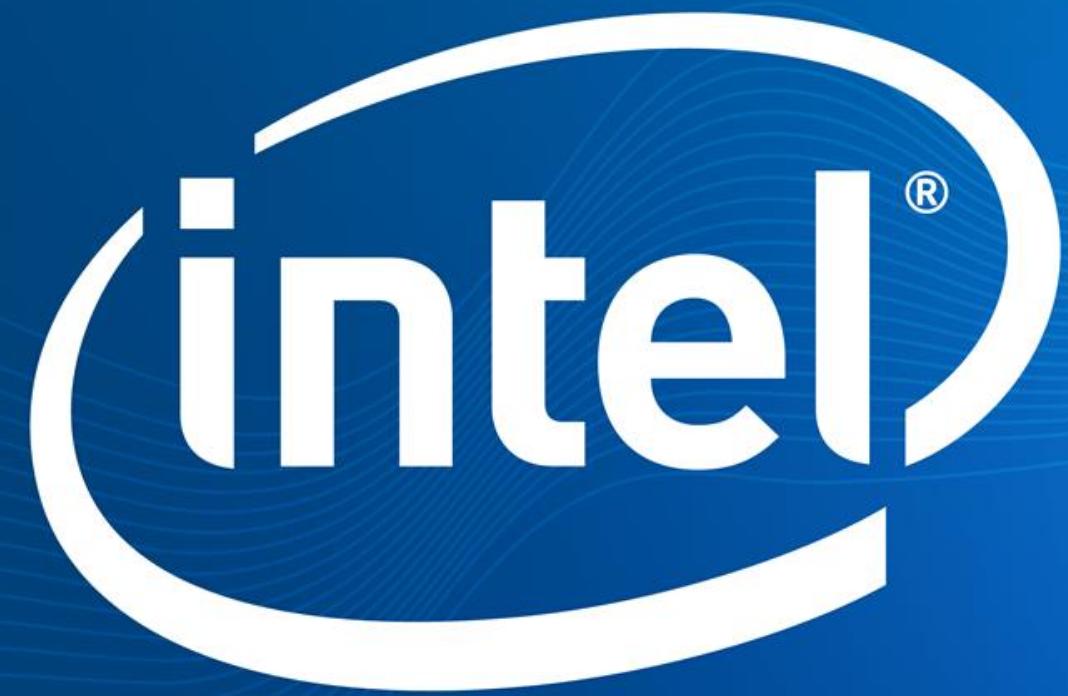
Инструменты анализа – Intel® VTune™

COMPOSER EDITION		PROFESSIONAL EDITION	CLUSTER EDITION
BUILD	ANALYZE	SCALE	
Compilers & Libraries	Analysis Tools	Cluster Tools	
C / C++, Fortran Compilers	Intel® Math Kernel Library Intel® Data Analytics Acceleration Library Intel Threading Building Blocks C++ Threading	Intel® VTune™ Amplifier Performance Profiler Intel® Inspector Memory & Thread Debugger Intel® Advisor Vectorization Optimization Thread Prototyping & Flow Graph Analysis	Intel® MPI Library Message Passing Interface Library Intel® Trace Analyzer & Collector MPI Tuning & Analysis Intel® Cluster Checker Cluster Diagnostic Expert System
Intel® Integrated Performance Primitives Image, Signal & Data Processing			
Intel® Distribution for Python* High Performance Python			

DISCLOSURES

Intel Technology and Manufacturing Day 2017 occurs during Intel's "Quiet Period," before Intel announces its 2017 first quarter financial and operating results. Therefore, presenters will not be addressing first quarter information during this year's program.

Statements in this presentation that refer to forecasts, future plans and expectations are forward-looking statements that involve a number of risks and uncertainties. Words such as "anticipates," "expects," "intends," "goals," "plans," "believes," "seeks," "estimates," "continues," "may," "will," "would," "should," "could," and variations of such words and similar expressions are intended to identify such forward-looking statements. Statements that refer to or are based on projections, uncertain events or assumptions also identify forward-looking statements. Such statements are based on management's expectations as of March 28, 2017, and involve many risks and uncertainties that could cause actual results to differ materially from those expressed or implied in these forward-looking statements. Important factors that could cause actual results to differ materially from the company's expectations are set forth in Intel's earnings release dated January 26, 2017, which is included as an exhibit to Intel's Form 8-K furnished to the SEC on such date. Additional information regarding these and other factors that could affect Intel's results is included in Intel's SEC filings, including the company's most recent reports on Forms 10-K, 10-Q and 8-K reports may be obtained by visiting our Investor Relations website at www.intc.com or the SEC's website at www.sec.gov.



INTEL® INNOVATION DAY

ВРИТМЕ ТЕХНОЛОГИИ